Please amend the paragraph beginning at page 77, line 18 to page 78, line 2, as

follows:

Specifically, when the detected speed of the car reaches the first overspeed, a

command signal is output to a safety circuit, so a drive power source for the drive device

(hoisting machine) for raising and lowering the car is cut off and braking is applied to the

rotation of the drive sheave by a brake, thereby bringing the car to a sudden stop. Further,

when the detected speed of the car reaches the second overspeed, a command signal is output

to the safety device as described in each of the above embodiments, thereby directly brining

bringing the car to a sudden stop.

Please amend the paragraph beginning at page 78, line 24 to page 79, line 6, as

follows:

The ROM 209 stores a program for executing the computation processing of the

position detecting portion 202, the speed detecting portion 203, the set value setting portion

204, and the comparing/determining portion 205. The CPU 208 executes the computation

processing (digital computation) at each computation cycle based on the program stored in

the ROM 209. The RAM 210 temporarily stores data used in the computation by the CPU

[[15]] <u>208</u>.

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